

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	8207721
<b>Application Number:</b>	10518772
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	4888
<b>Title of Invention:</b>	Circuit arrangement for a display device which can be operated in a partial mode
<b>First Named Inventor/Applicant Name:</b>	Christopher Rodd Speirs
<b>Customer Number:</b>	94518
<b>Filer:</b>	Alan A. Limbach
<b>Filer Authorized By:</b>	
<b>Attorney Docket Number:</b>	348162-982220
<b>Receipt Date:</b>	12-AUG-2010
<b>Filing Date:</b>	01-AUG-2005
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<b>Application Type:</b>	U.S. National Stage under 35 USC 371

### Payment information:

Submitted with Payment	no
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed (SB/08)	IDS_filed_8-12-10_348162-992 220.pdf	126211 2472d35bf01ee7aa9bab58886898c7a0382 ae1ec	no	3

### Warnings:

### Information:

This is not an USPTO supplied IDS fillable form					
2	Foreign Reference	JP2002351414A.pdf	1016142	no	21
			3de3049a5a4839296c0d3935057e686dc77d22b		
<b>Warnings:</b>					
<b>Information:</b>					
3	Foreign Reference	JP2002351414_Abtract.pdf	135205	no	1
			071c85e78eef81540305a09aaf98fbd4cf2e12b		
<b>Warnings:</b>					
<b>Information:</b>					
<b>Total Files Size (in bytes):</b>			1277558		
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